

FIG. 1

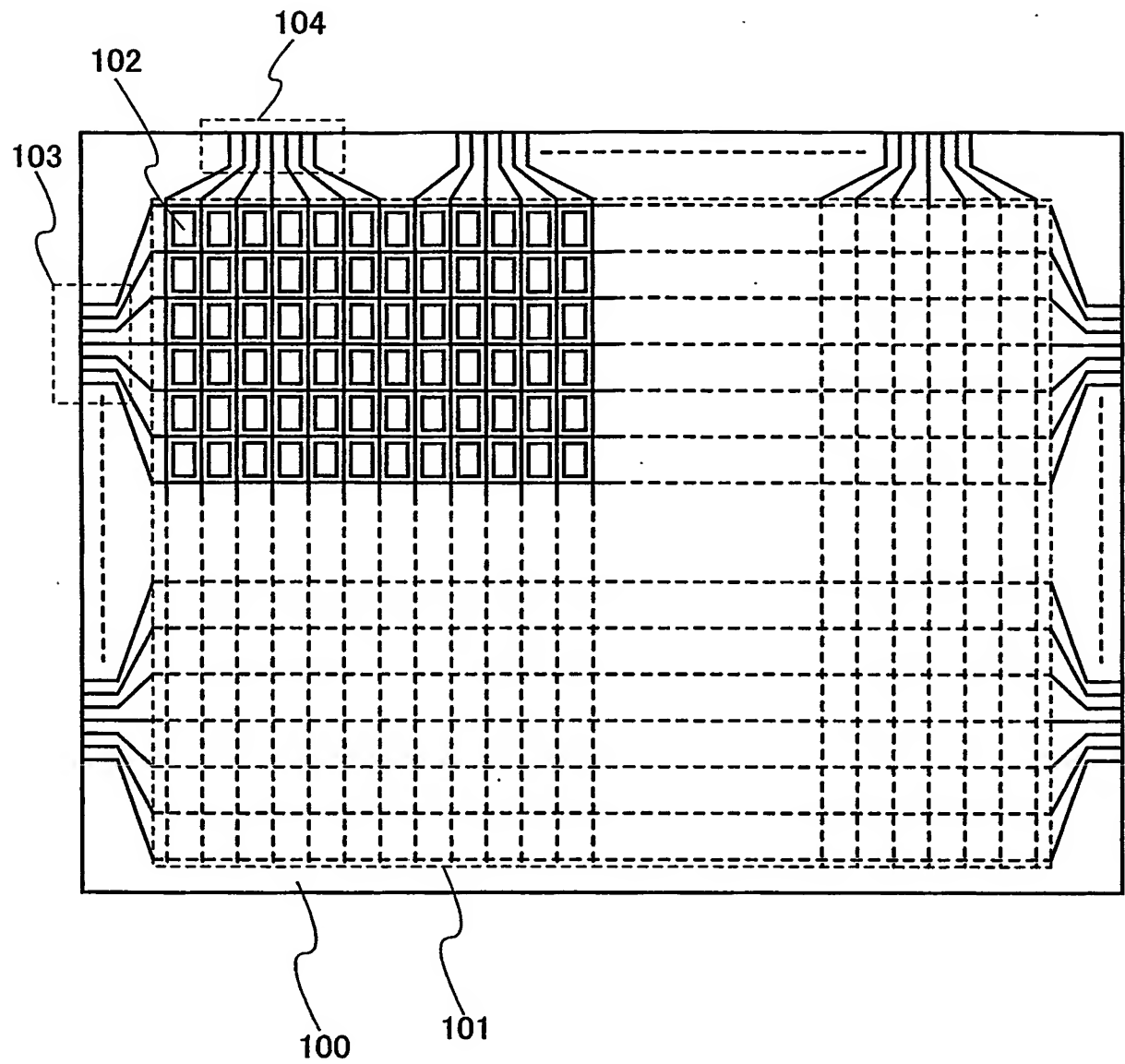


FIG. 2

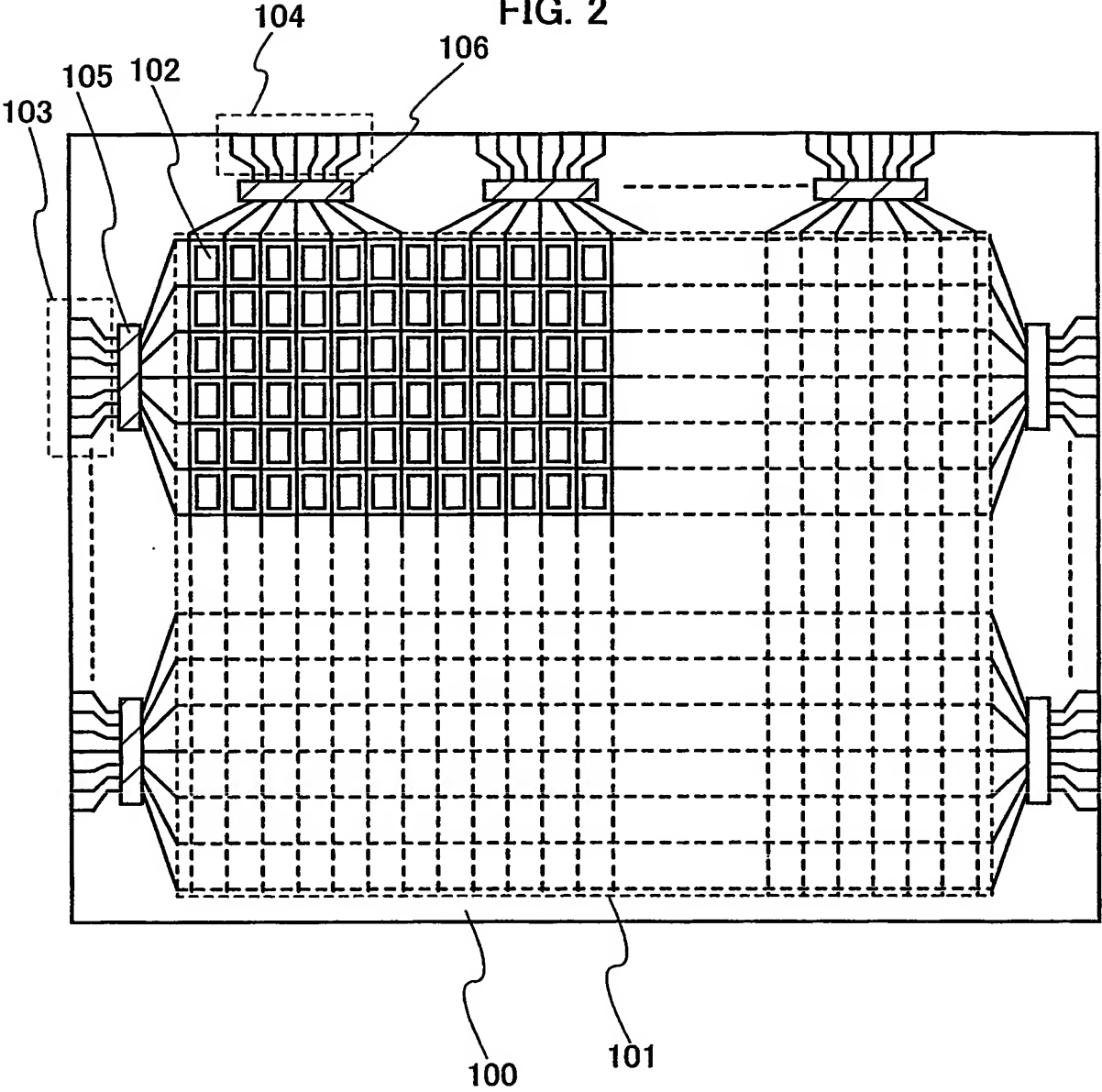
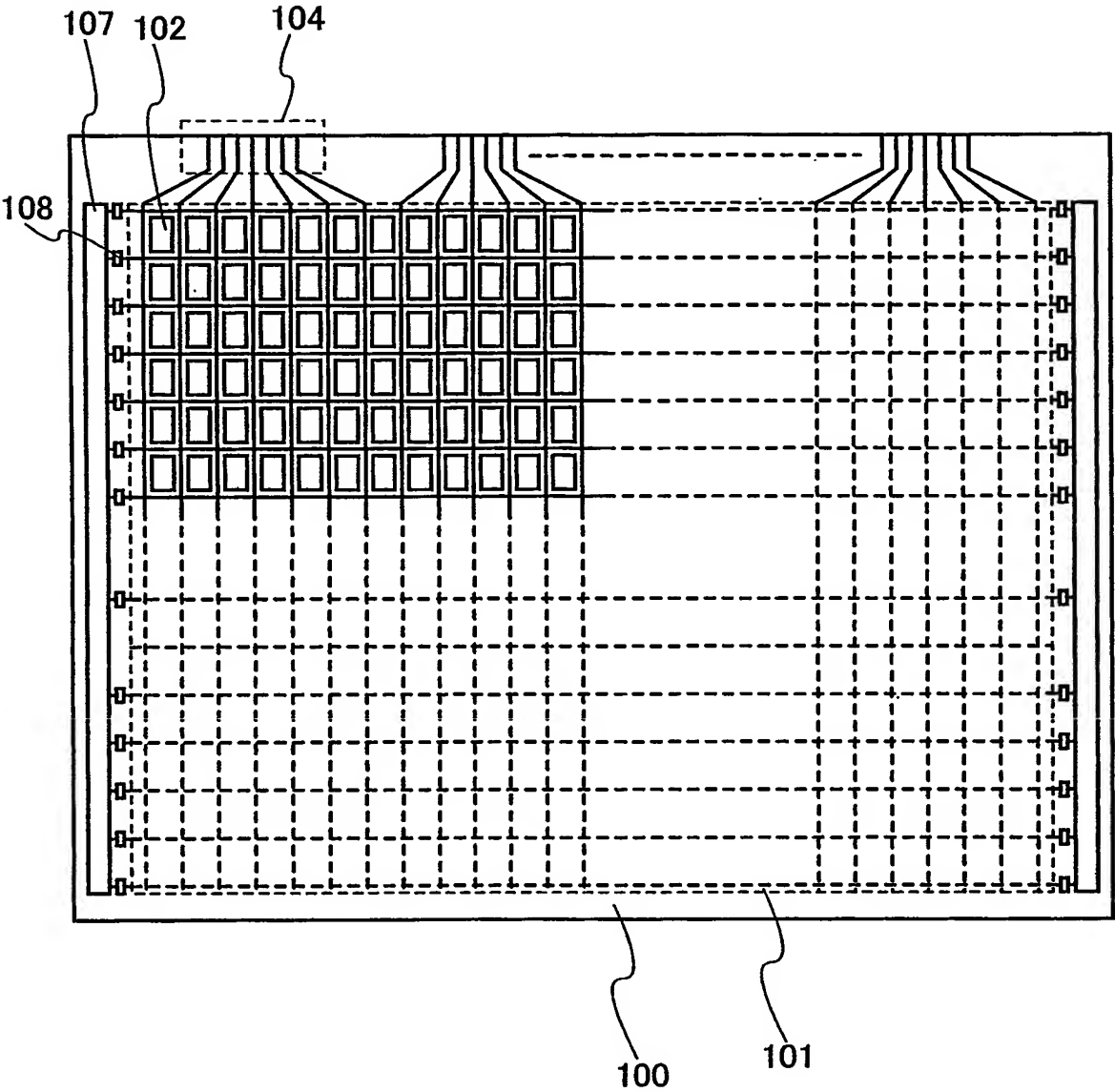


FIG. 3



A - B CROSS SECTION

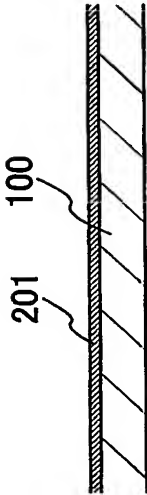


FIG. 4A

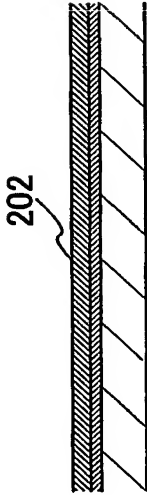


FIG. 4B



FIG. 4C

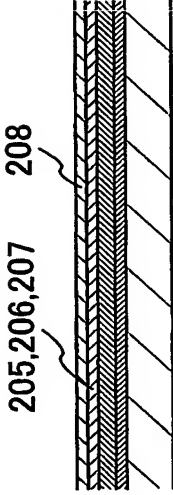


FIG. 4D

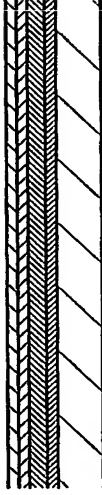
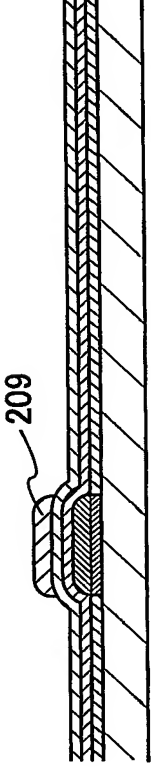
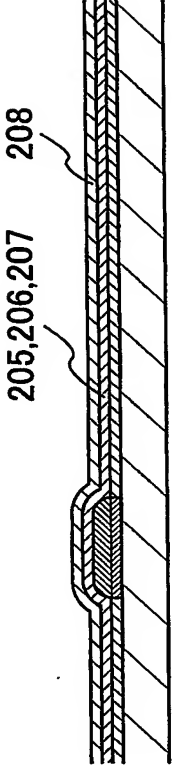
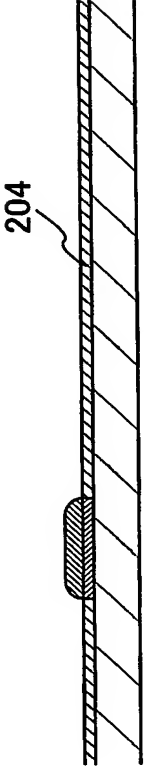
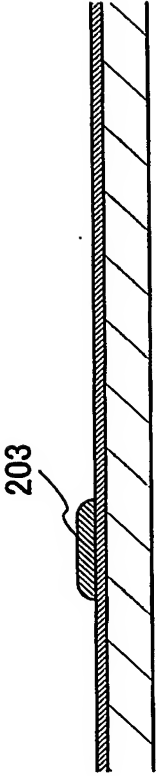
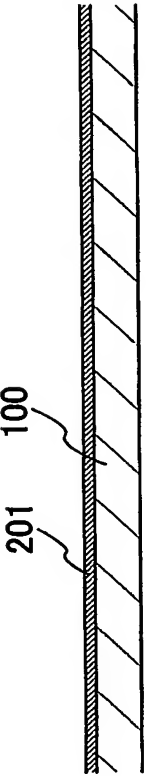


FIG. 4E

C - D CROSS SECTION



A - B CROSS SECTION

210

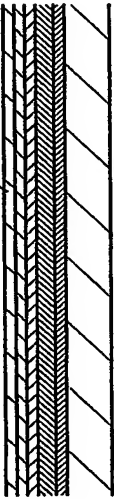


FIG. 5A

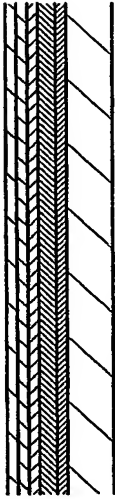


FIG. 5B



FIG. 5C

214



FIG. 5D

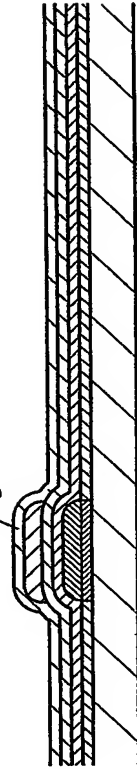
215



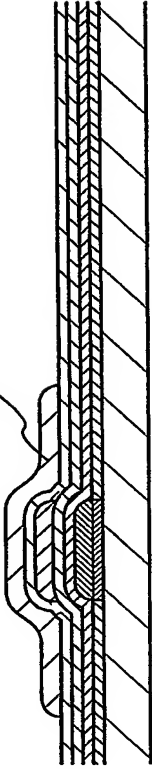
FIG. 5E

C - D CROSS SECTION

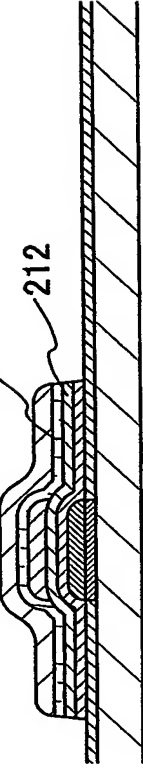
210



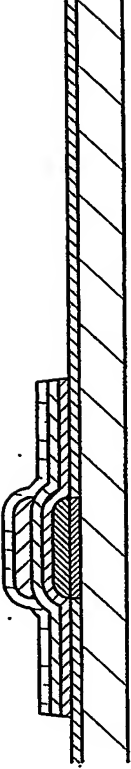
211



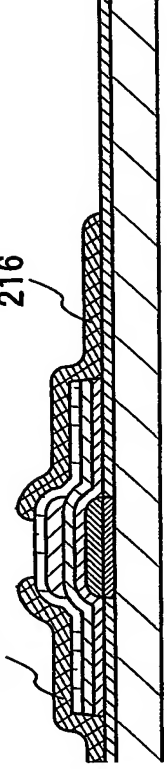
213



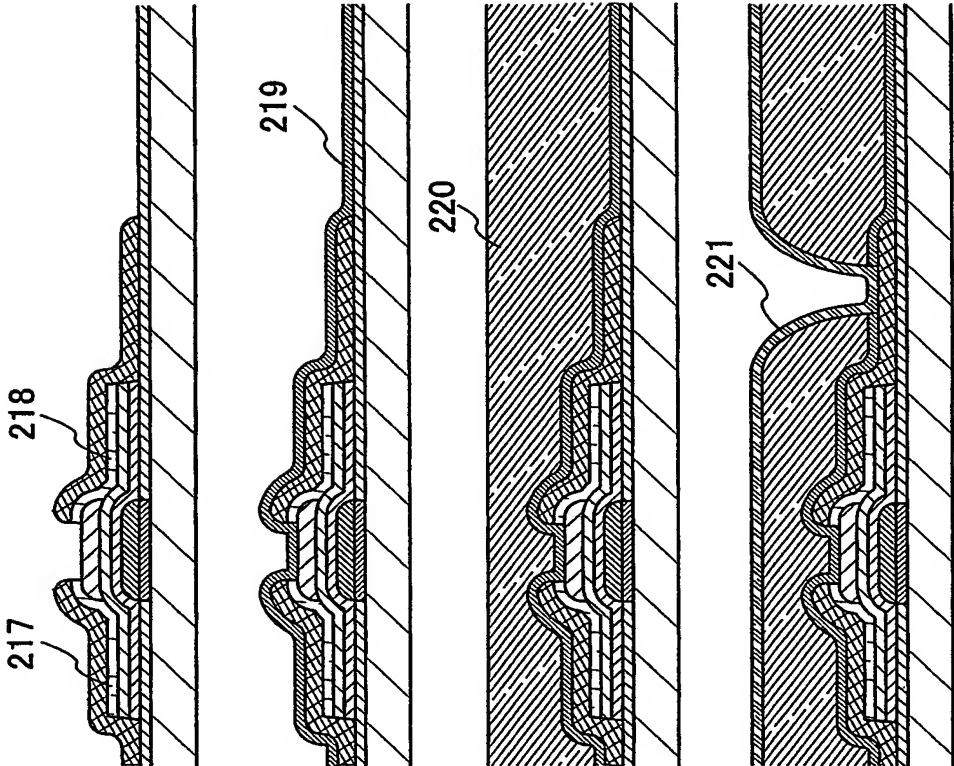
212



215



C - D CROSS SECTION



A - B CROSS SECTION

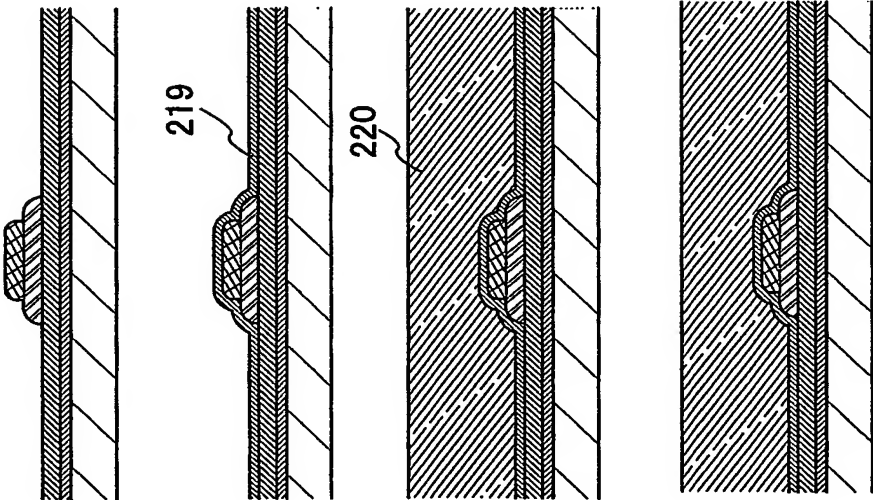


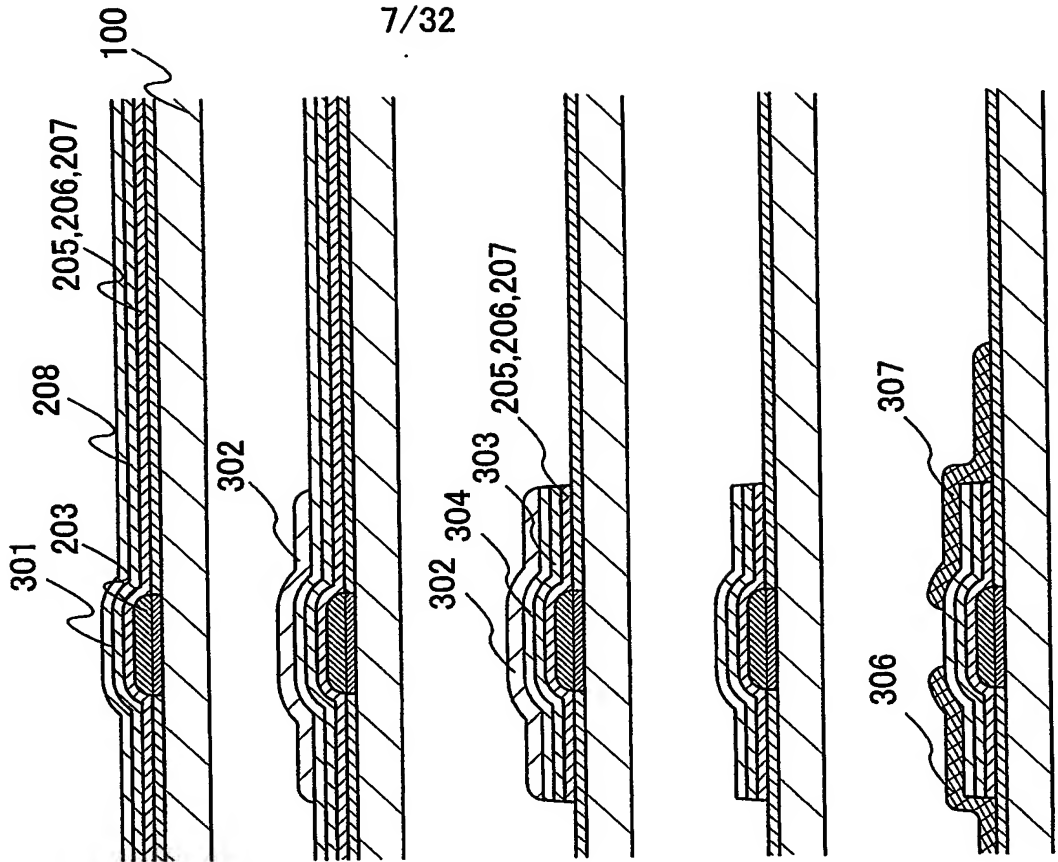
FIG. 6A

FIG. 6B

FIG. 6C

FIG. 6D

C - D CROSS SECTION



A - B CROSS SECTION

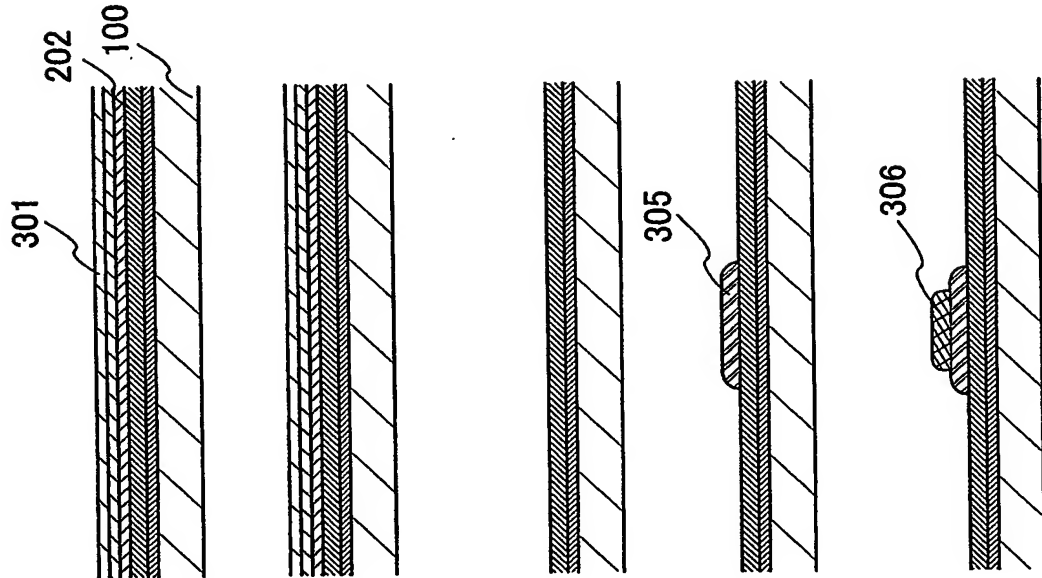


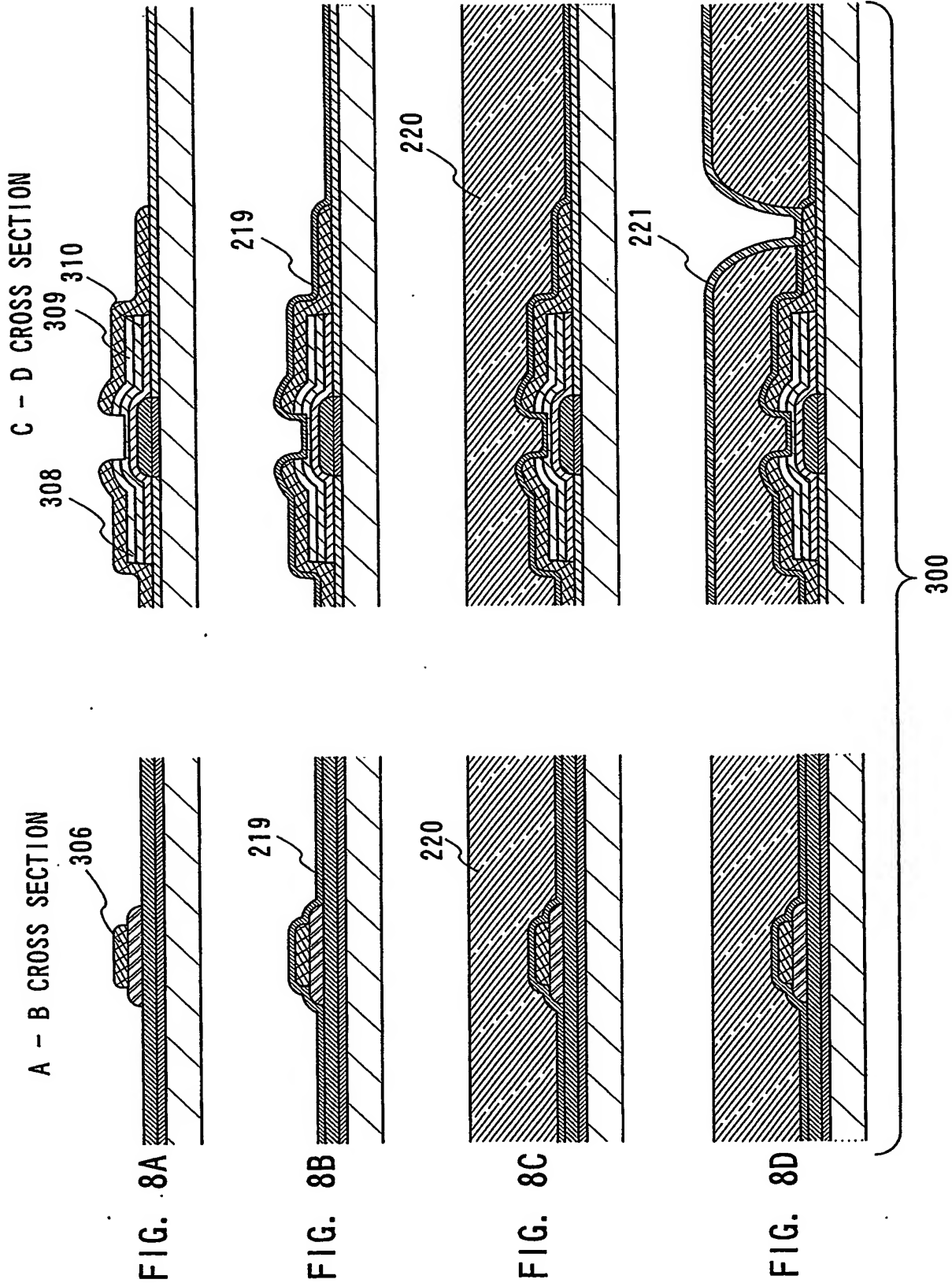
FIG. 7A

FIG. 7B

FIG. 7C

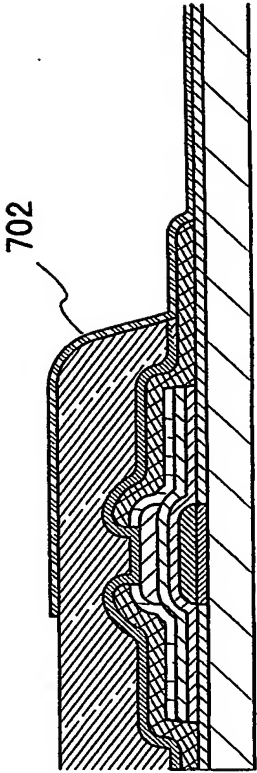
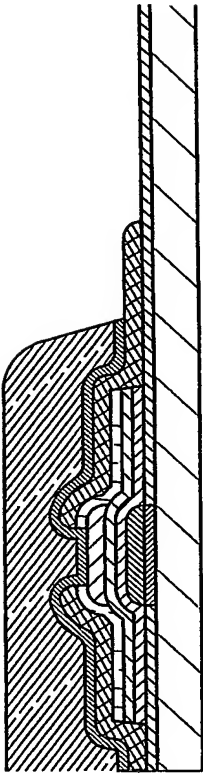
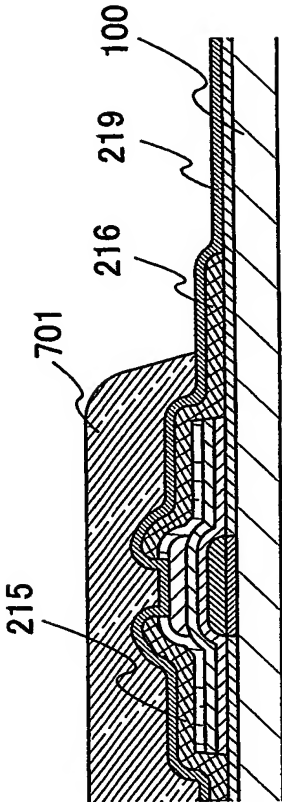
FIG. 7D

FIG. 7E





C - D CROSS SECTION



700

A - B CROSS SECTION

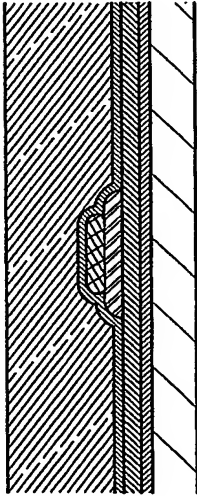
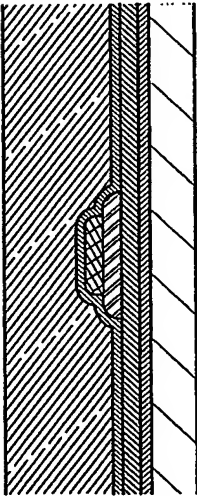
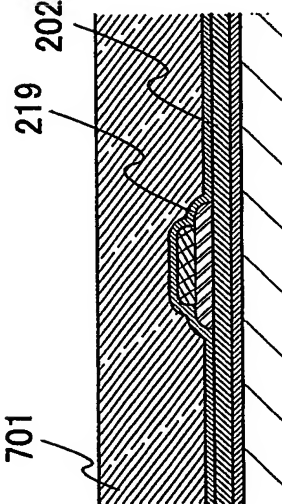
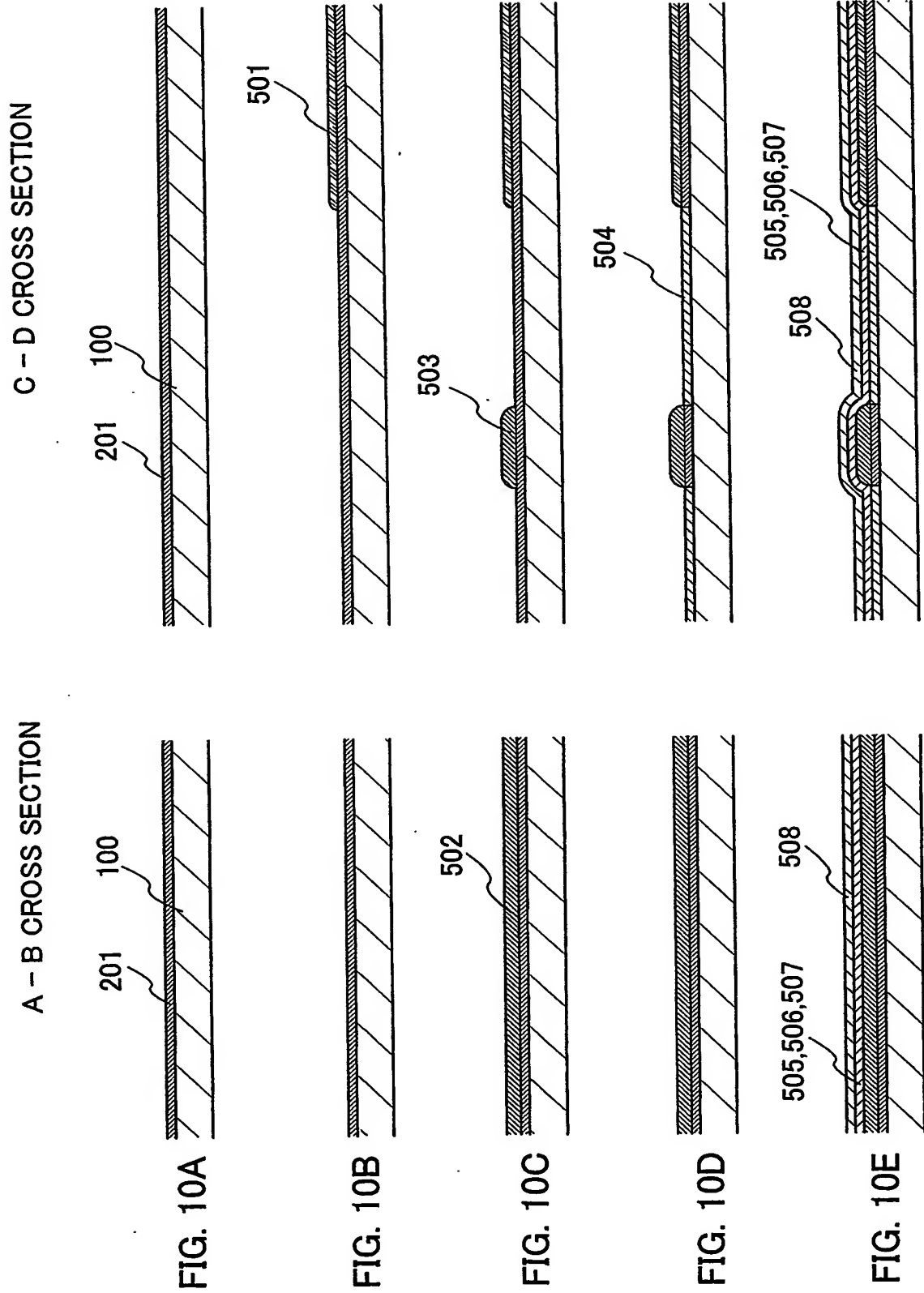


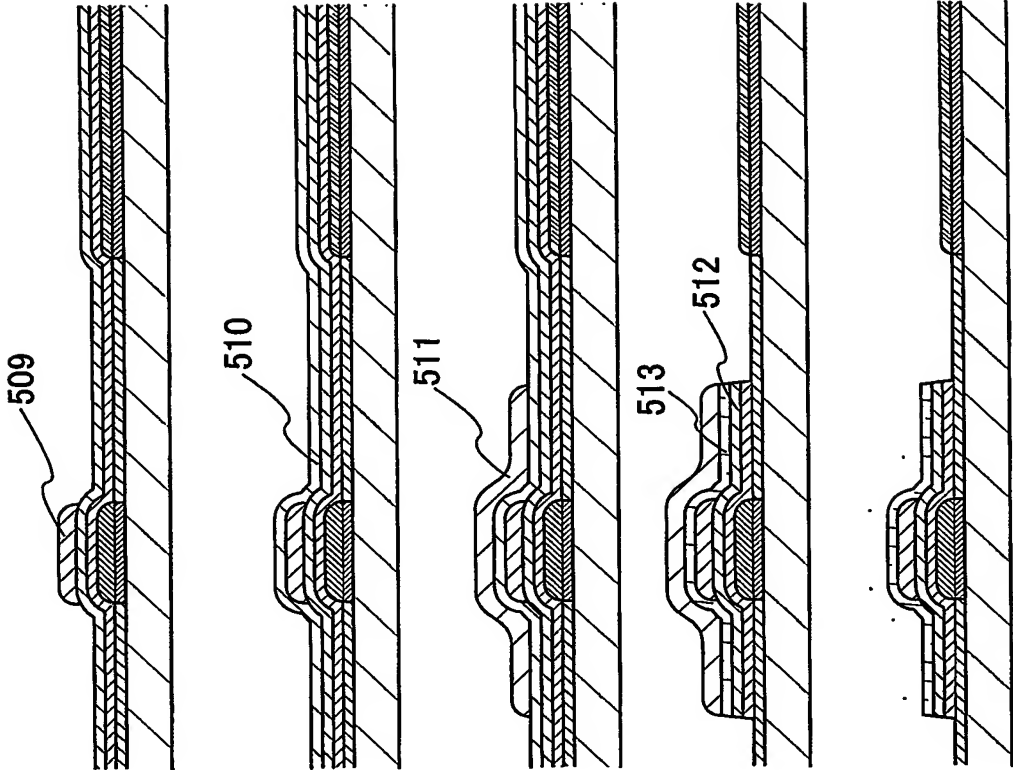
FIG. 9A

FIG. 9B

FIG. 9C



C - D CROSS SECTION



A - B CROSS SECTION

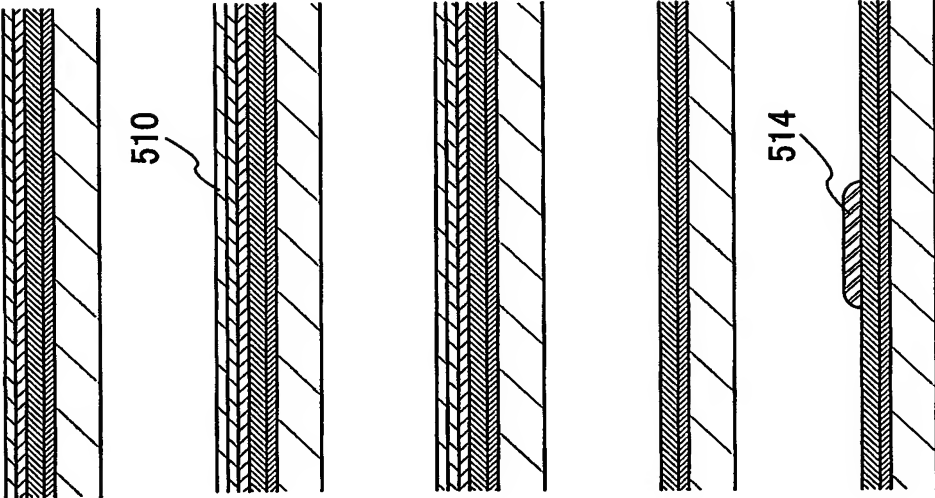


FIG. 11A

FIG. 11B

FIG. 11C

FIG. 11D

FIG. 11E

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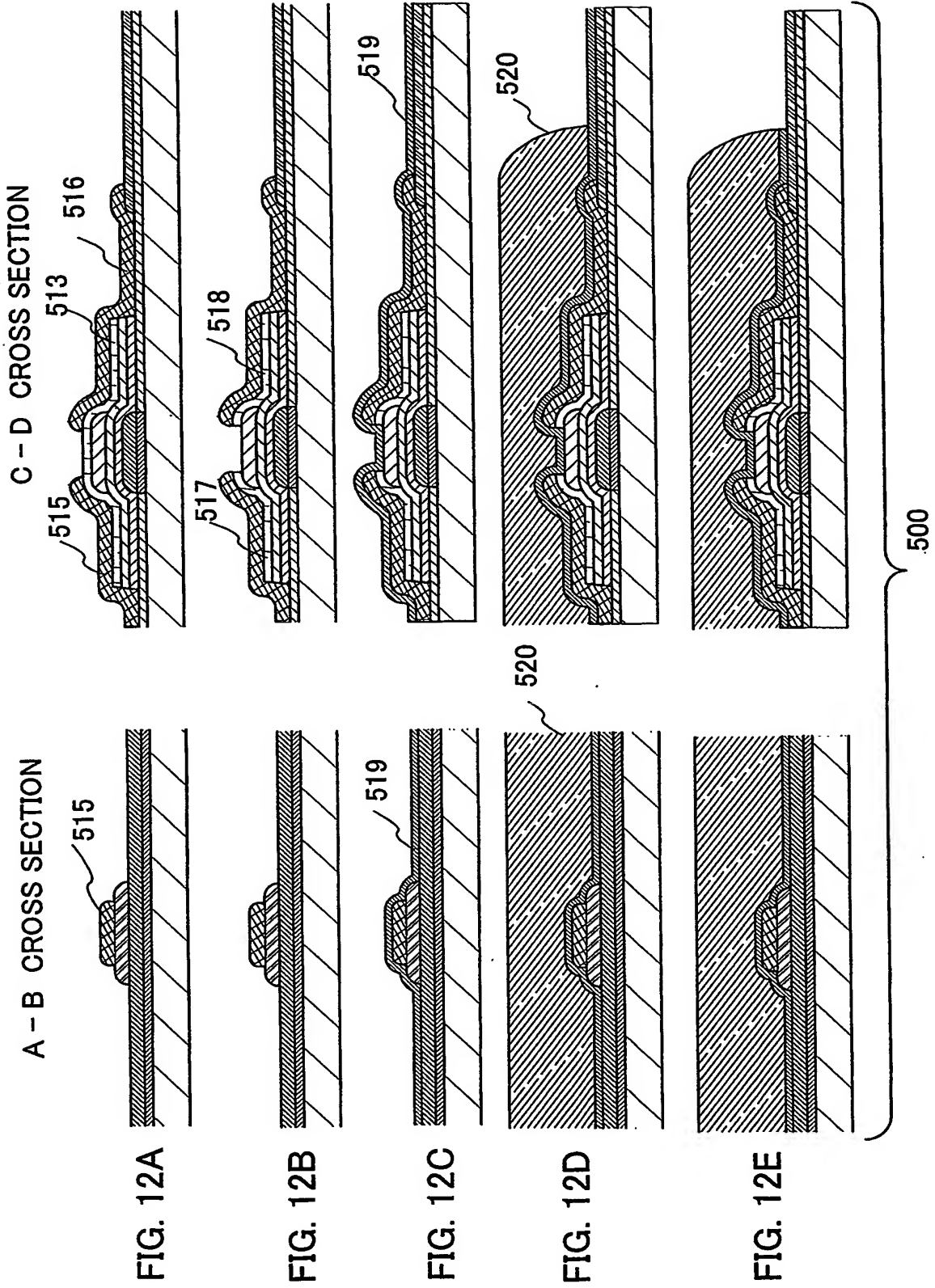
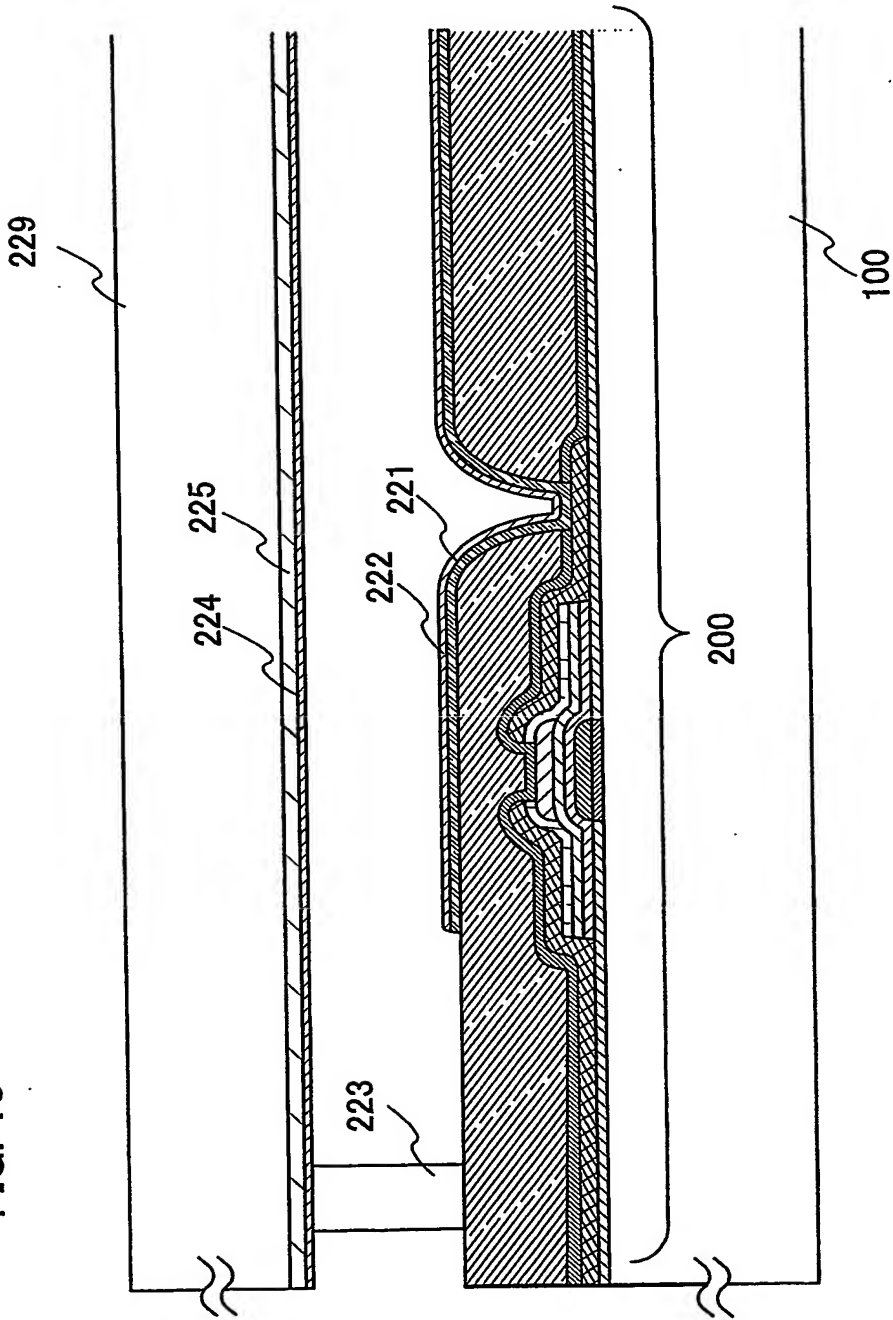


FIG. 13



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FIG. 14A

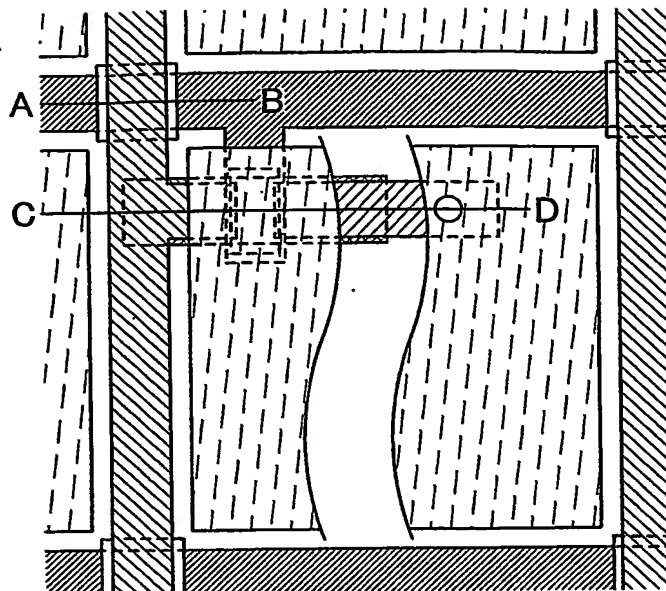


FIG. 14B

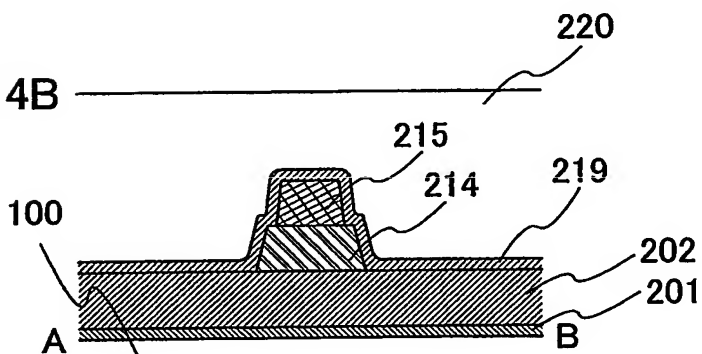
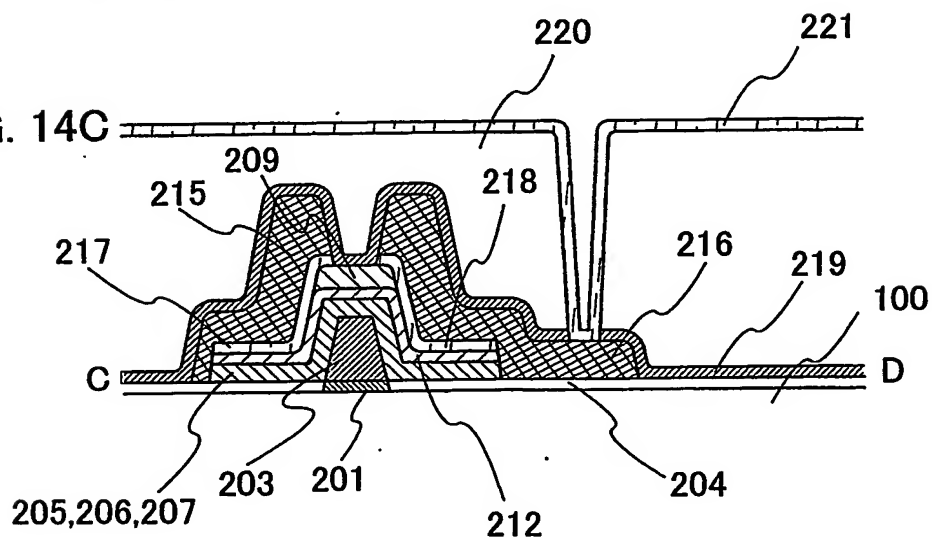


FIG. 14C



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FIG. 15A

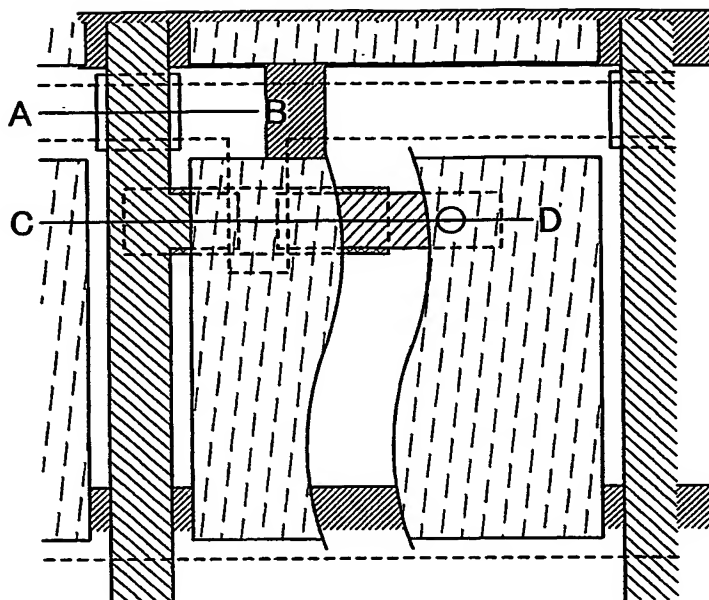


FIG. 15B

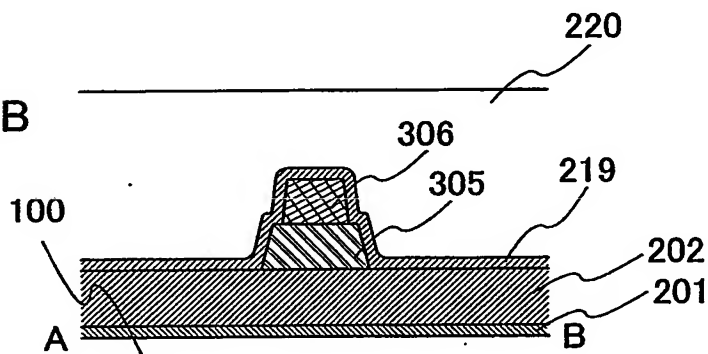
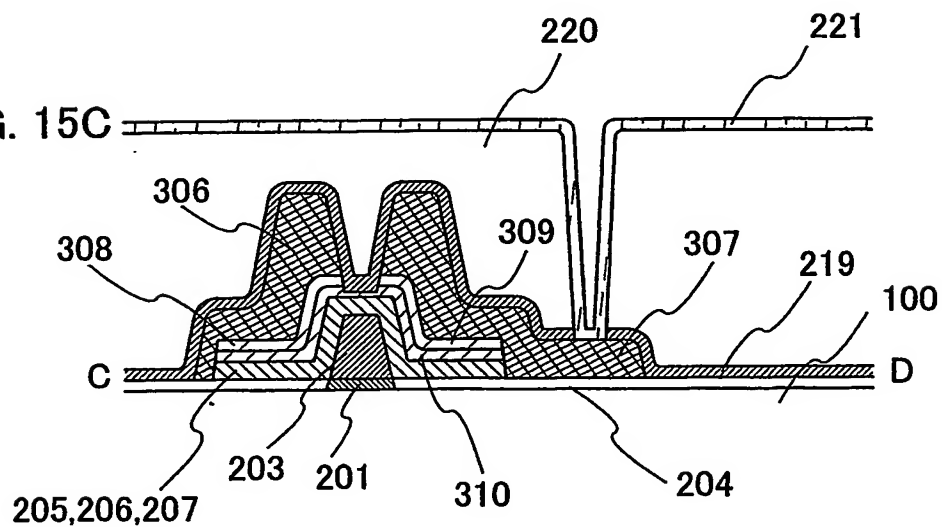


FIG. 15C



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FIG. 16A

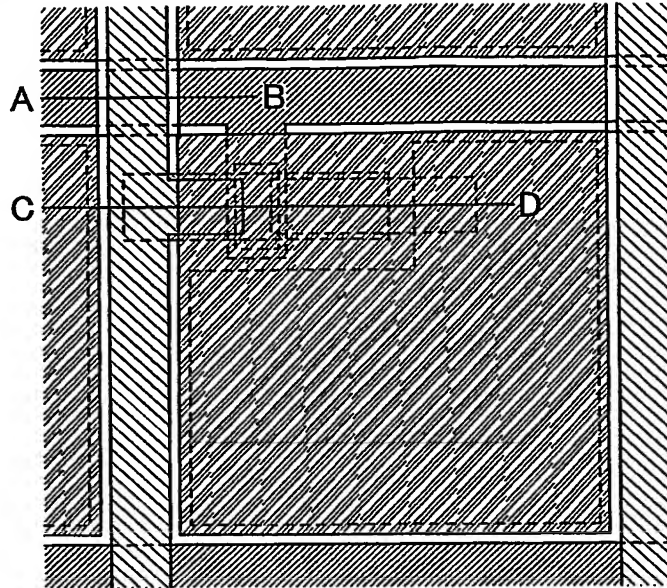


FIG. 16B

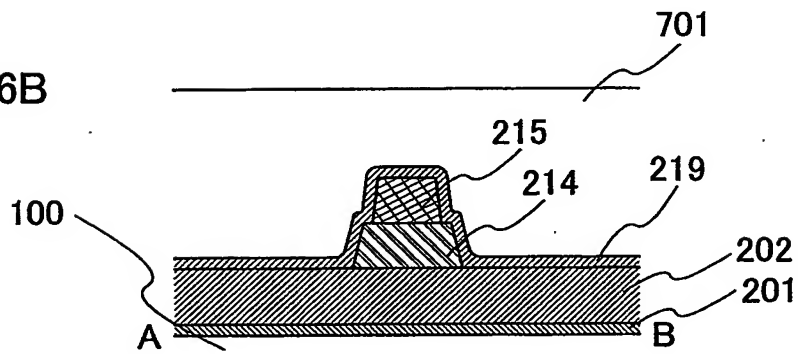
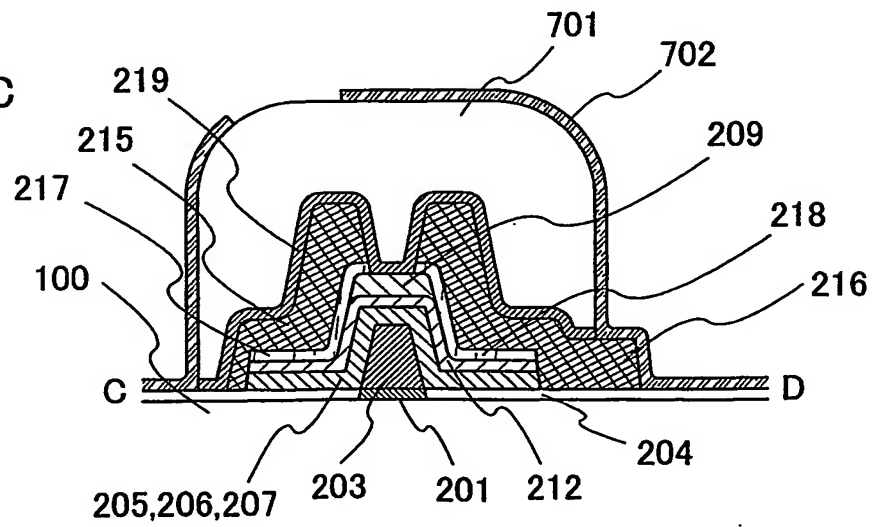


FIG. 16C





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FIG. 17A

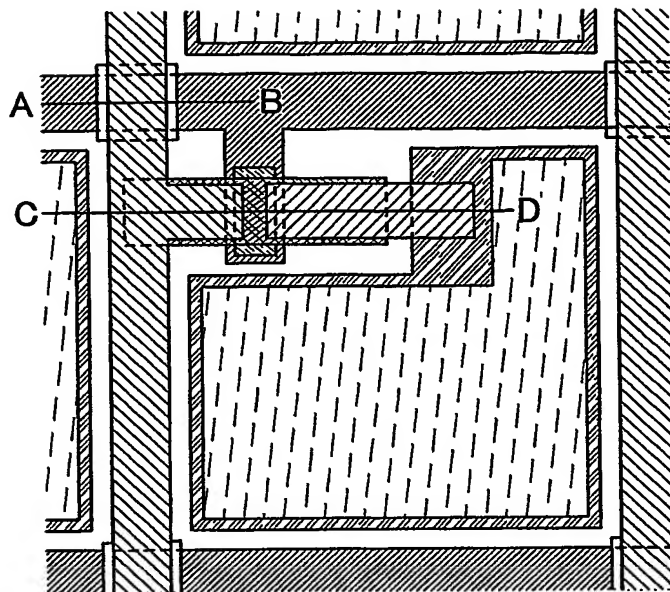


FIG. 17B

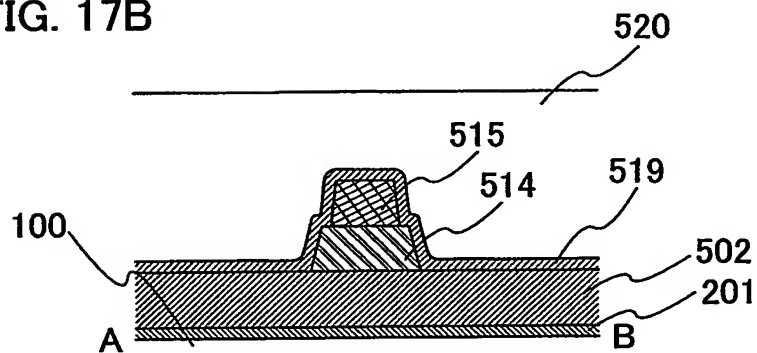
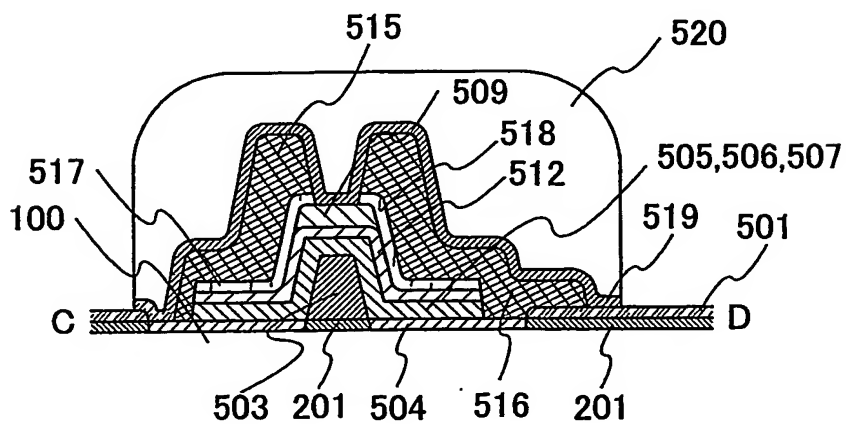
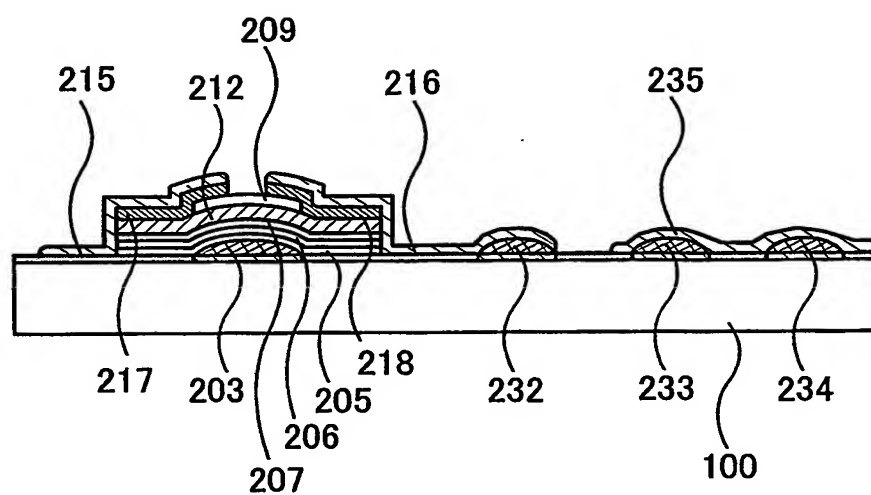


FIG. 17C



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FIG. 18



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FIG. 19A

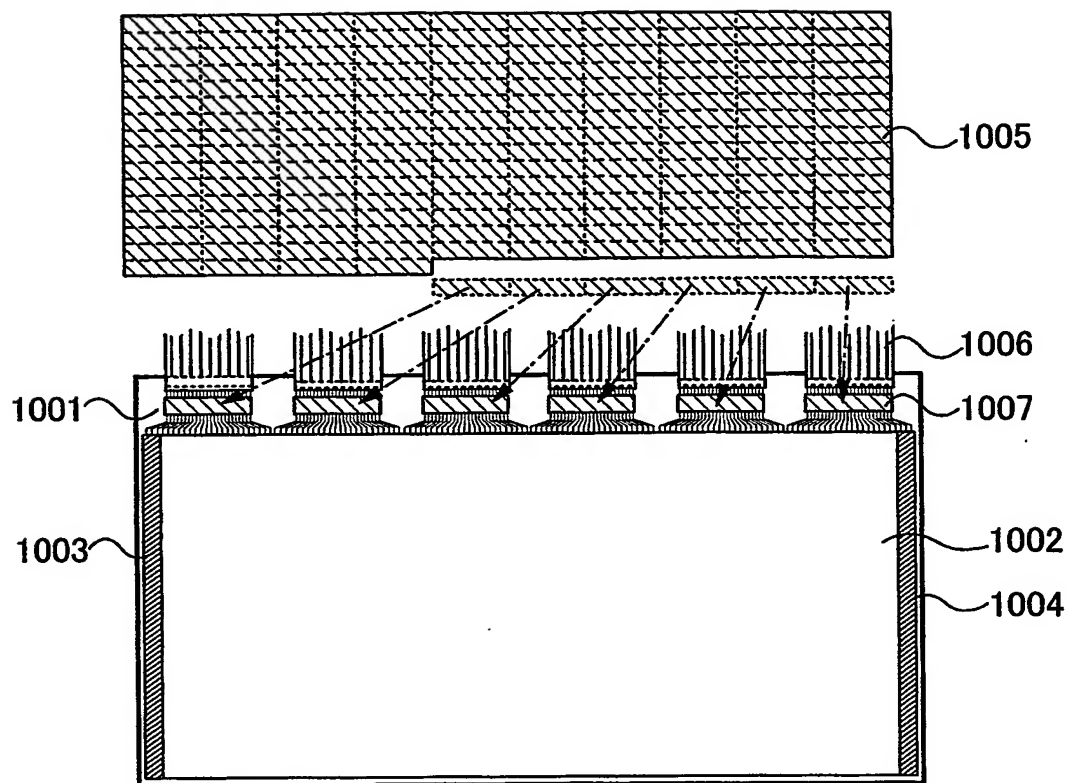
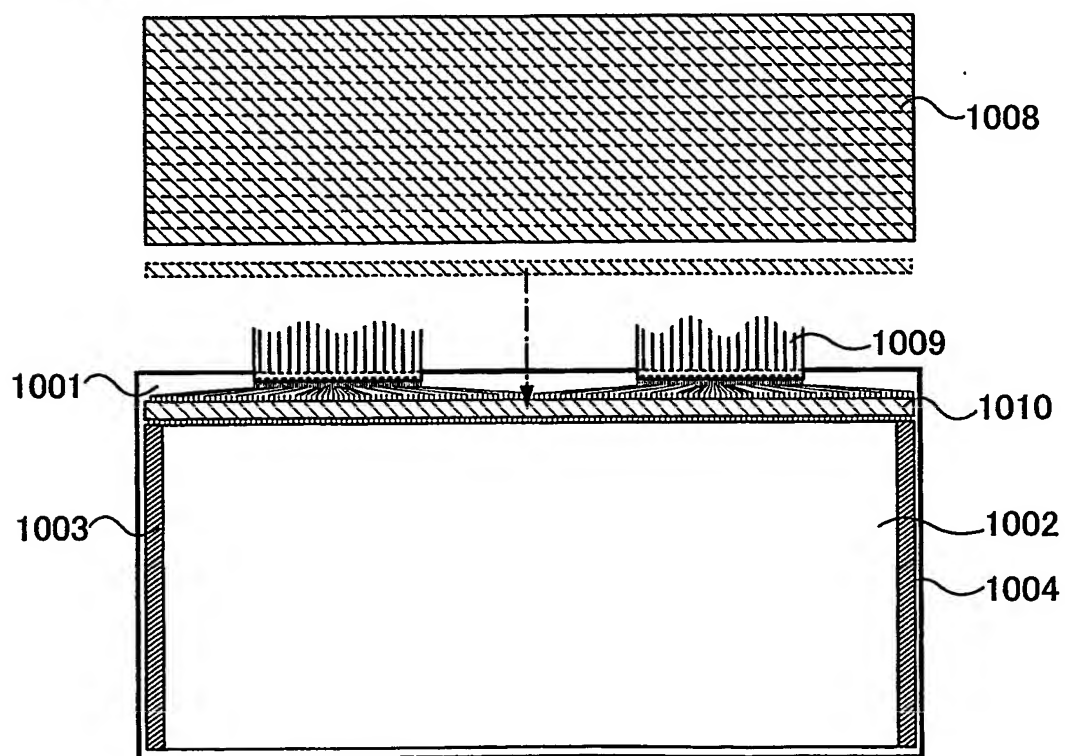


FIG. 19B



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FIG. 20A

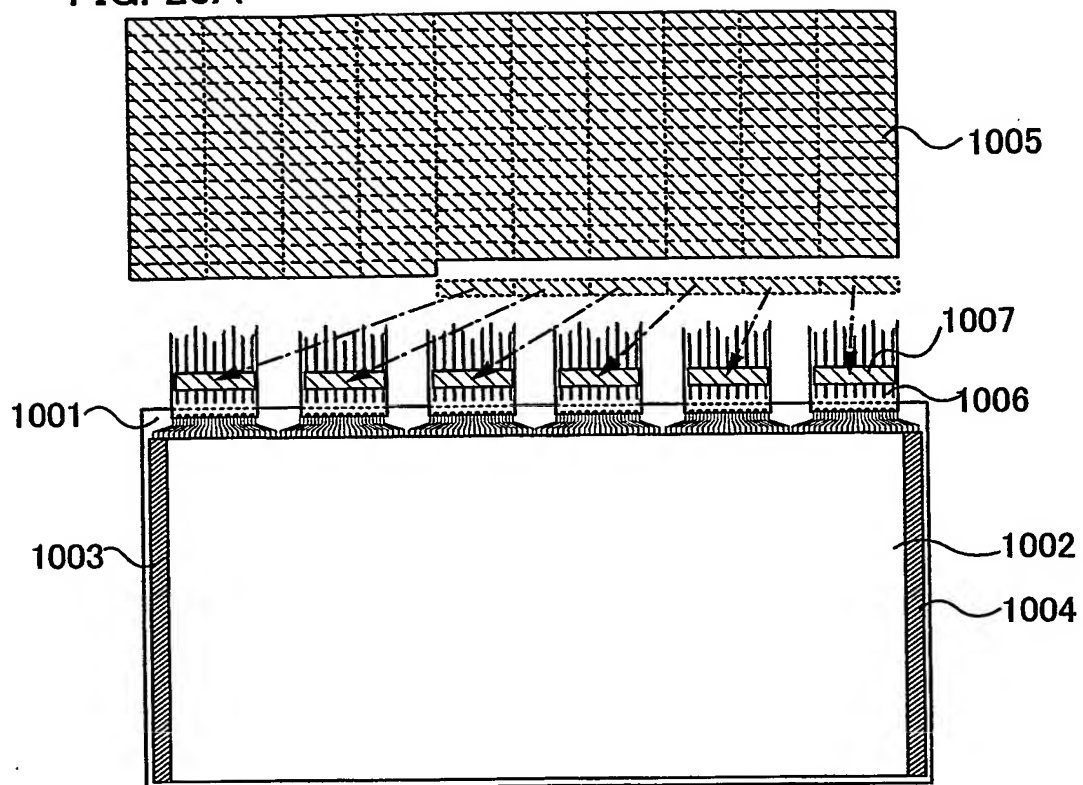
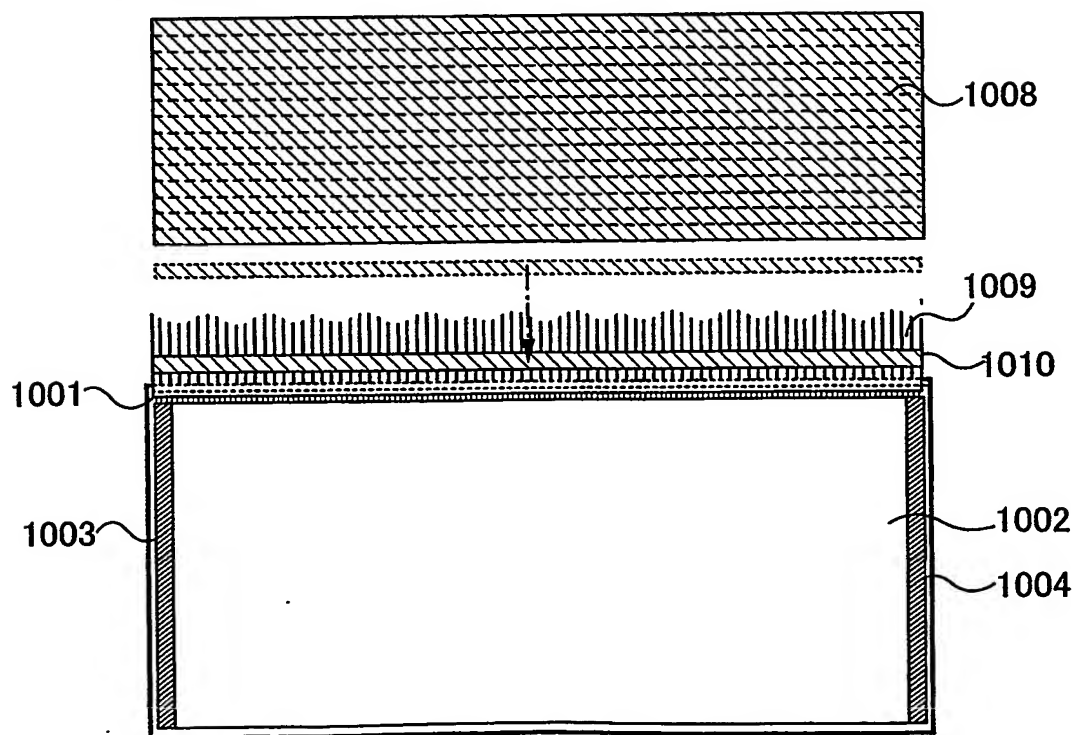


FIG. 20B



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FIG. 21A

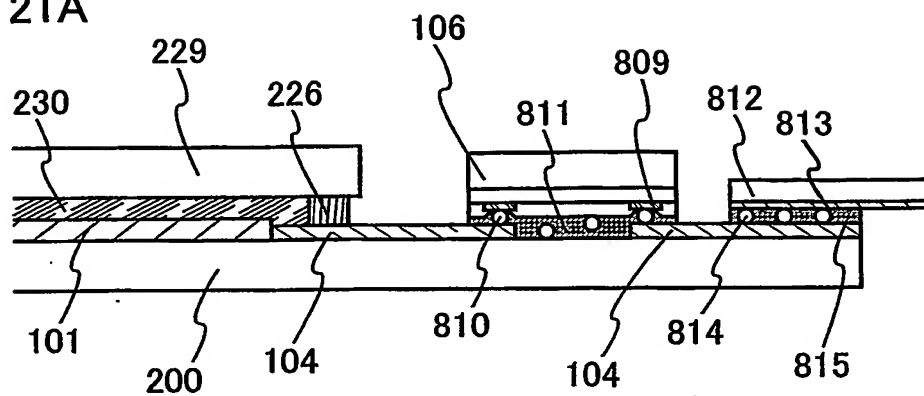


FIG. 21B

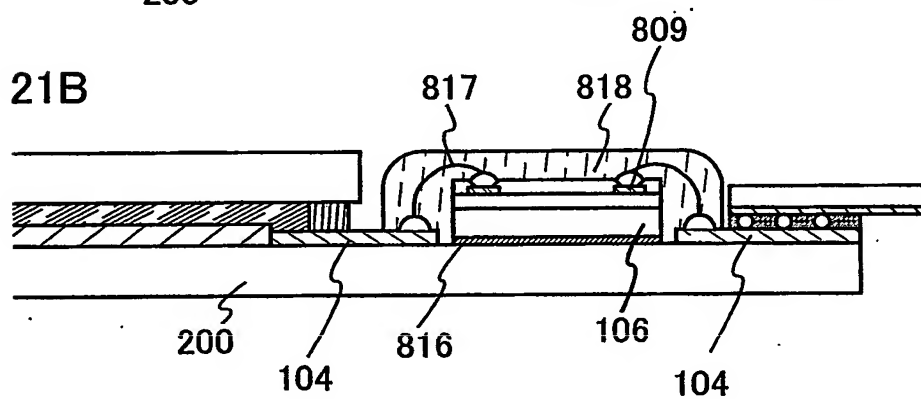
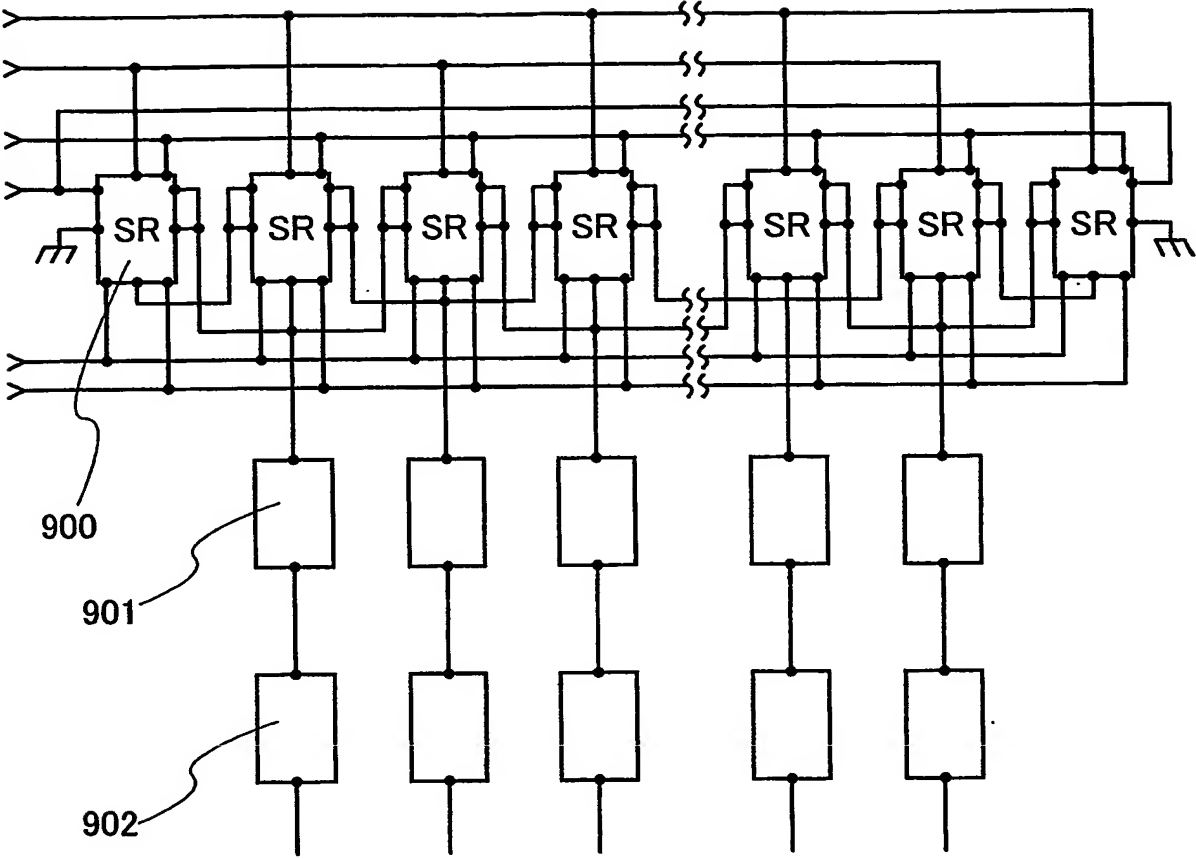
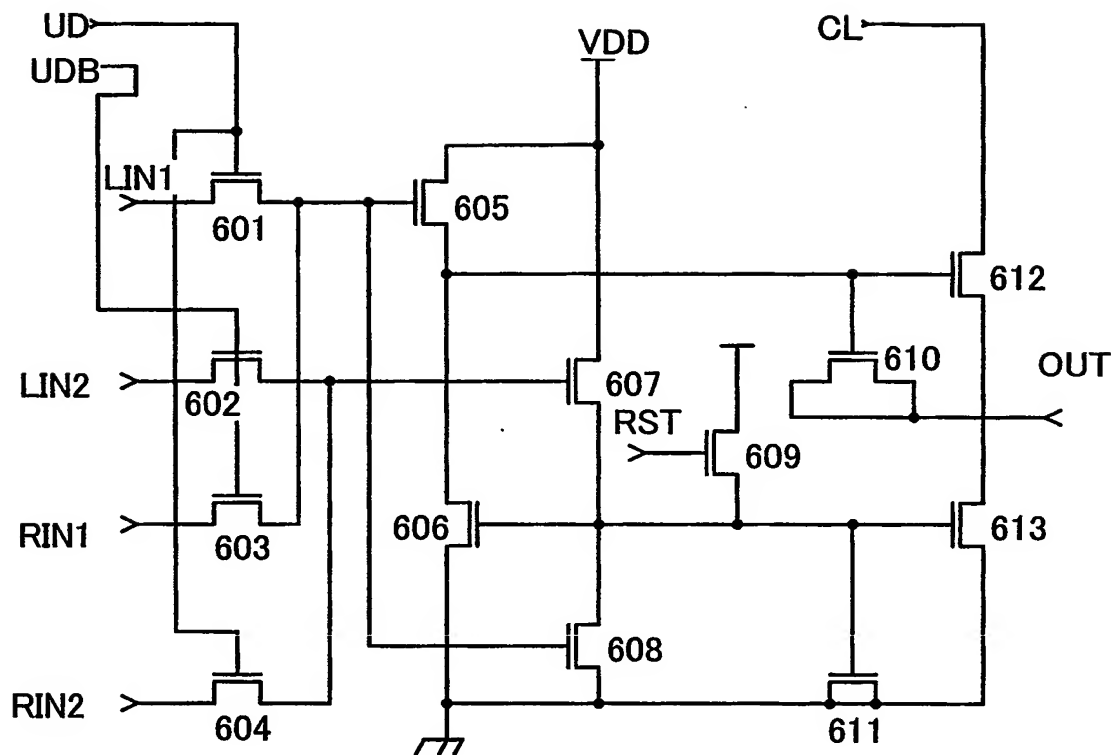


FIG. 22



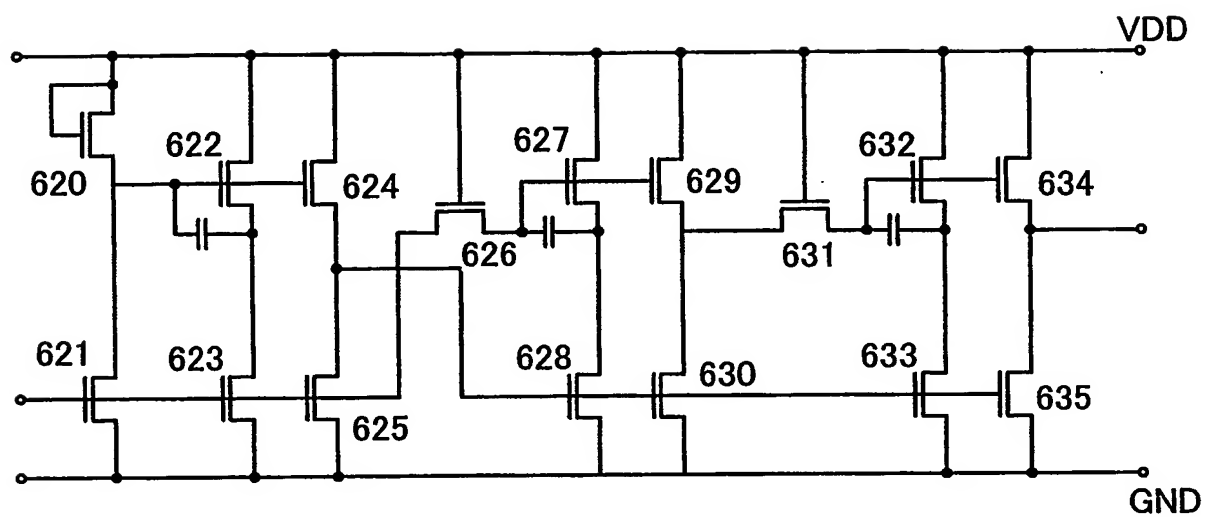
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FIG. 23



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FIG. 24





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FIG. 25

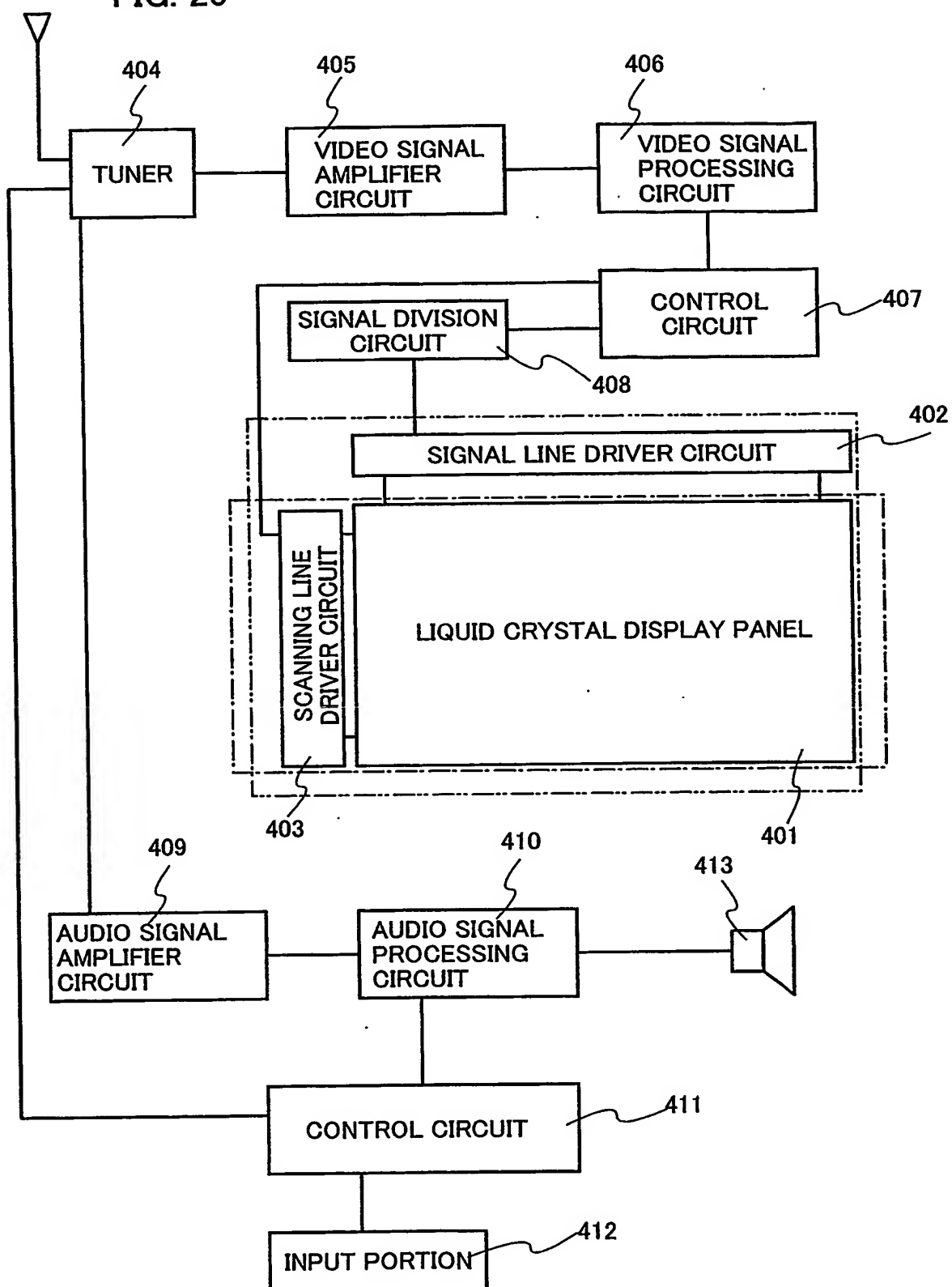
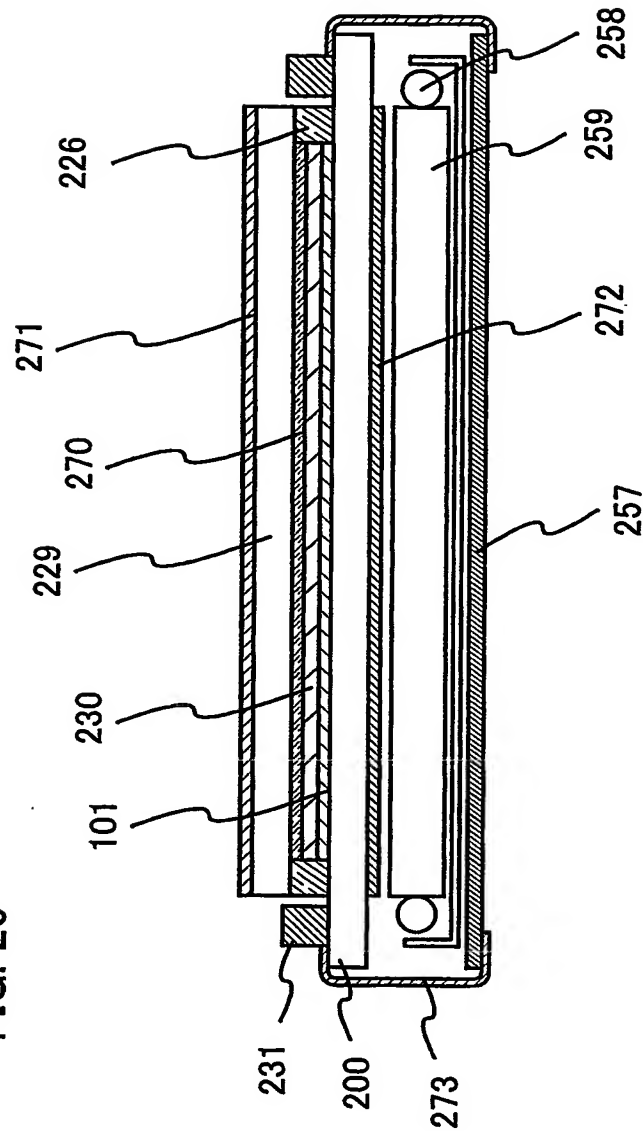


FIG. 26



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FIG. 27

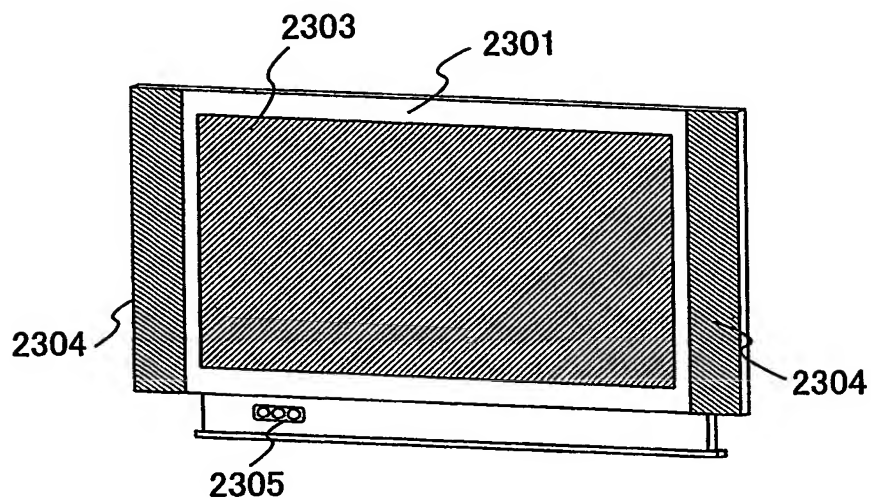
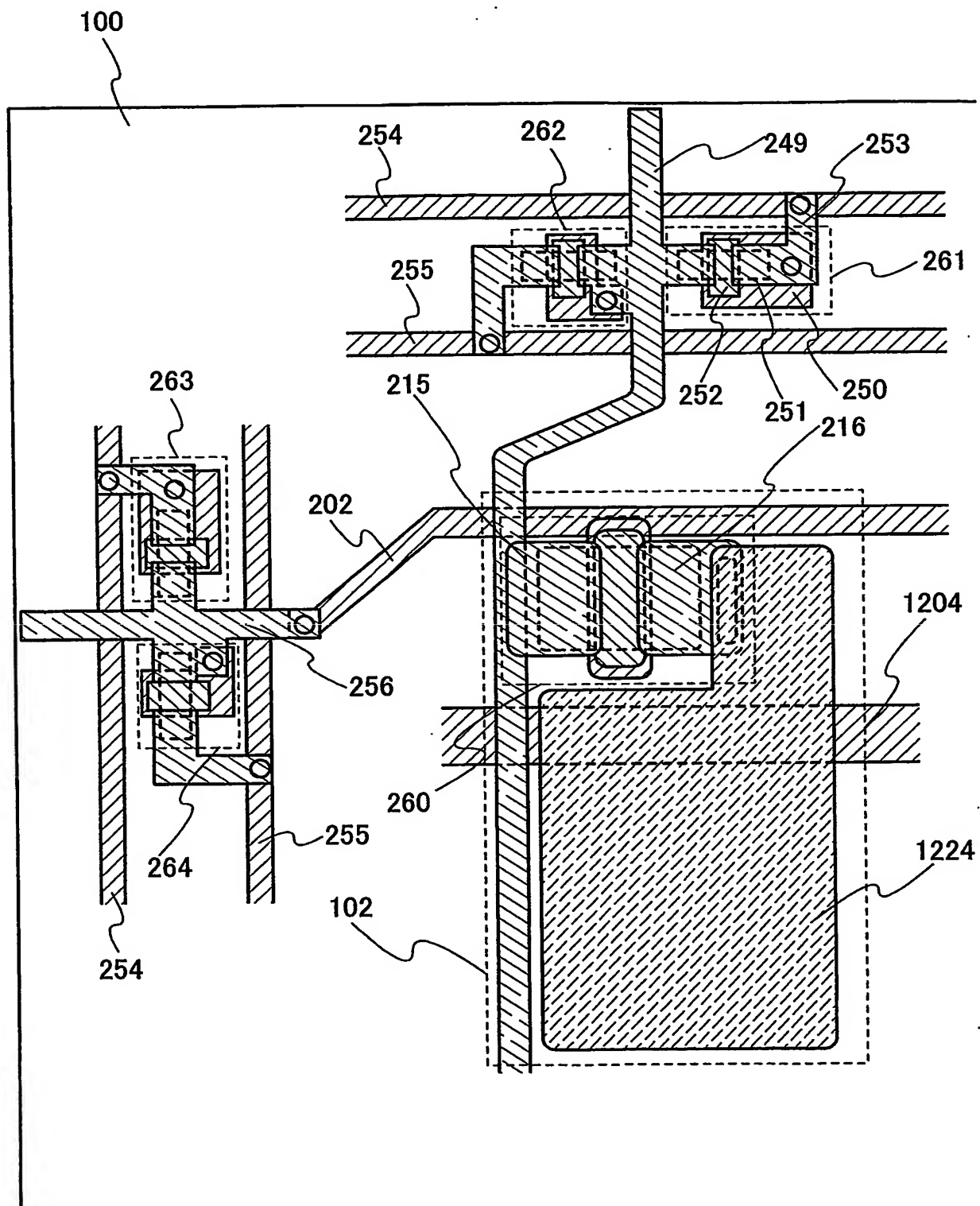
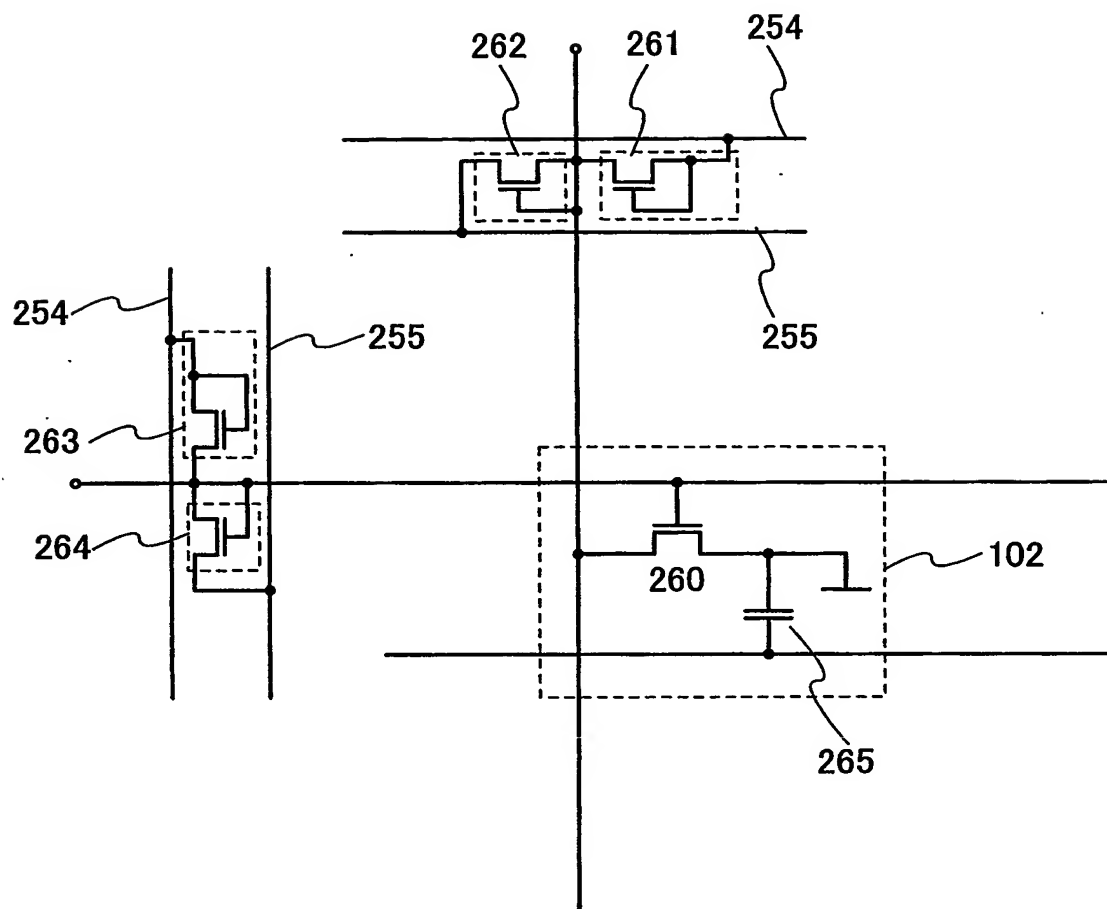


FIG. 28



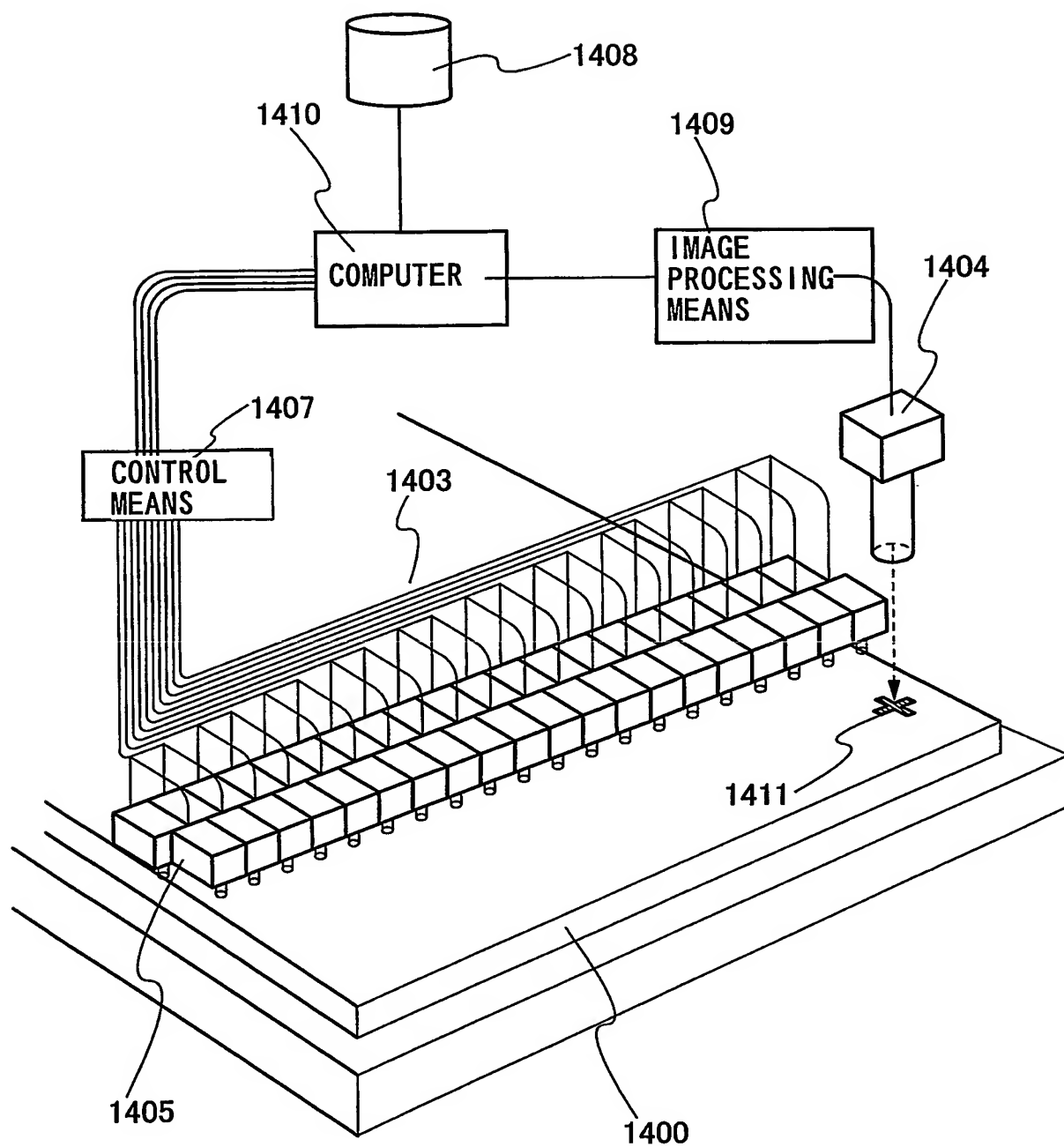
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FIG. 29



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FIG. 30



EXPLANATION OF REFERENCE

100: substrate, 101: pixel portion, 102: pixel, 103: scan line input terminal, 104: signal line input terminal, 105: driver IC, 106: driver IC, 107: scan line driver circuit, 108: protection diode, 200: TFT substrate, 201: adhesion improving layer, 202: gate wiring layer, 203: gate electrode, 204: insulating layer, 205: insulating layer, 206: insulating layer, 207: insulating layer, 208: semiconductor film, 209: channel protective film, 210: semiconductor film, 211: mask, 212: semiconductor film, 213: semiconductor film, 214: interlayer insulating film, 215: source wiring, 216: drain wiring, 217: semiconductor film, 218: semiconductor film, 219: protective layer, 220: insulating layer, 221: pixel electrode layer, 222: insulating layer, 223: sealant, 224: insulating layer, 225: conductive layer, 226: sealant, 229: counter substrate, 230: liquid crystal layer, 231: terminal, 232: connection wiring layer, 233: connection wiring layer, 234: connection wiring layer, 235: connection wiring layer, 249: wiring, 250: gate electrode, 251: semiconductor film, 252: insulating layer, 253: wiring layer, 254: common voltage wiring, 255: wiring, 256: signal wiring layer, 257: circuit board, 258: cold cathode tube, 259: light guide plate, 260: TFT, 261: protection diode, 262: protection diode, 265: capacitor, 270: colored layer, 271: polarizer, 272: polarizer, 273: flexible printed circuit, 300: TFT substrate, 301: semiconductor film, 302: mask, 303: semiconductor film, 304: semiconductor film, 305: interlayer film, 306: source wiring, 307: drain wiring, 308: semiconductor film, 309: semiconductor film, 310: semiconductor film, 401: pixel area, 402: signal line driver circuit, 403: scan line driver circuit, 404: tuner, 405: video signal amplifier circuit, 406: video signal processing circuit, 407: control circuit, 408: signal splitter circuit, 409: audio signal amplifier circuit, 410: audio signal processing circuit, 411: control circuit, 412: input unit, 413: speaker, 500: TFT substrate, 501: pixel electrode layer, 502: gate wiring layer, 503: gate electrode, 504: insulating layer, 505: insulating layer, 506: insulating layer, 507: insulating layer, 508: semiconductor layer, 509: channel protective film, 510: semiconductor film, 511: mask, 512: semiconductor film, 513: semiconductor film, 514: interlayer insulating film, 515: source wiring, 516: drain wiring, 517: semiconductor film, 518: semiconductor film, 519: insulating layer,

520: insulating layer, 601: TFT, 602: TFT, 603: TFT, 604: TFT, 605: TFT, 606: TFT, 607: TFT, 608: TFT, 609: TFT, 610: TFT, 611: TFT, 612: TFT, 613: TFT, 620: TFT, 621: TFT, 622: TFT, 623: TFT, 624: TFT, 625: TFT, 626: TFT, 627: TFT, 630: TFT, 631: TFT, 632: TFT, 633: TFT, 634: TFT, 635: TFT, 700: TFT substrate, 701: insulating layer, 702: pixel electrode layer, 809: input-output terminal, 810: conductive particle, 811: resin, 812: FPC, 813: wiring, 814: conductive particle, 815: resin, 816: adhesive material, 817: Au wire, 818: sealer resin, 1001: substrate, 1002: pixel area, 1003: driver circuit, 1004: driver circuit, 1005: substrate, 1006: tape, 1007: driver IC, 1008: substrate, 1009: tape, 1010: driver IC, 1204: capacitor line, 1224: pixel electrode, 1400: substrate, 1403: droplet discharge means, 1404: imaging means, 1405: head, 1407: control means, 1408: storage medium, 1409: image processing means, 1410: computer, 1411: marker, 1500: pulse output circuit, 1501: buffer circuit, 1502: pixel, 2301: case, 2303: display screen, 2304: speaker, and 2305: operation switches.